

	L #	Hits	Search Text	DBs
1	L1	3383	(361/767,768,769,770,771,777,778,779,782,783,784,785,790,791,803).ccls.	USP AT; US-P GPU B
2	L2	3868	(257/691,698,700,723,724,728,786,664).ccls.	USP AT; US-P GPU B
3	L3	1462	(333/33,247).ccls.	USP AT; US-P GPU B
4	L4	1463	324/765.ccls.	USP AT; US-P GPU B
5	L5	340	(363/144,146,147).ccls.	USP AT; US-P GPU B
6	L6	9930	1 2 3 4 5	USP AT; US-P GPU B
7	L7	2438	6 and (convert\$3 invert\$3 regulat\$3 rectif\$ conversion inversion)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
8	L9	79	7 and ((power adj module\$1) (power adj pod\$1))	USP AT; US-P GPU B
9	L8	106	7 and adapter\$1	USP AT; US-P GPU B
10	L10	77	9 not 8	USP AT; US-P GPU B
11	L11	64	7 and interposer\$1	USP AT; US-P GPU B
12	L12	57	11 not (8 10)	USP AT; US-P GPU B
13	L13	42	7 and overvoltage\$1	USP AT; US-P GPU B
14	L14	3	(8 10 12) and 13	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
15	L15	39	13 not 14	USP AT; US-P GPU B
16	L16	0	361/91.ccls.	USP AT; US-P GPU B
17	L17	284	(361/91.1).ccls.	USP AT; US-P GPU B
18	L18	156	17 and (convert\$3 invert\$3 regulat\$3 rectif\$ conversion inversion)	USP AT; US-P GPU B
19	L19	139	18 and power	USP AT; US-P GPU B
20	L20	139	19 not (8 10 12)	USP AT; US-P GPU B
21	L21	8	17 and (adapter\$1 interposer\$1)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
22	L22	16	18 not (19 21)	USP AT; US-P GPU B
23	L23	126	17 not (19 21 22)	USP AT; US-P GPU B